Notice of References Cited

Application/Control No.	Applicant(s)/Pater	nt Under
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U.S. PATENT DOCUMENTS

	U.S. PATENT DOCUMENTS				
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-			
	В	US-			
	С	US-			
	D	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Perlinent Pages)			
	U	Lee, Reflow Soldering Processes and Troubleshooting SMT, BGA, CSP and Flip Chip Technologies			
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"A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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